



#3/B
501.20699VC1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: HORI et al

Serial No.:

Filed: December 28, 1989

For: SEMICONDUCTOR INTEGRATED CIRCUIT

Group: 236

Examiner: Baker, S.

PRELIMINARY AMENDMENT

Honorable Commissioner of
Patents and Trademarks
Washington, D.C. 20231

December 28, 1989

Sir:

Prior to examination on the merits of this application
please amend it as follows.

IN THE CLAIMS:

Please cancel claims 1 - 6 of the application without
prejudice or disclaimer of the subject matter contained therein
and add the following new claims.

Sub 65-7
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A semiconductor integrated circuit comprising:
a chip;
first circuits provided on said chip;
second circuits provided on said chip; and
voltage limiter means provided on said chip for reducing an

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